

## EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	3	(xdr and reset).ab.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/31 14:45
S1	1697	348/294	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/27 18:20
S2	840	348/294.ccls.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/27 18:20
S3	393	348/294.ccls.and reset	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/27 18:20
S4	247	348/294.ccls.and reset and @ay<="2002"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/27 18:48
S5	2	("6486504").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/27 18:47
S6	47	348/294.ccls. and reset.ab. and @ay<="2002"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/27 18:51
S7	21	348/294.ccls. and (voltage and reset).ab. and @ay<="2002"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/27 18:56

## EAST Search History

S8	18	348/294.ccls. and ((pulse or voltage) with reset).ab. and @ay<="2002"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/30 13:30
S9	5	"6348681"	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2007/07/30 13:30
S10	6	((("6348681") or ("20020043610") or ("6532040")).PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/30 14:35
S11	2	("20040174754").PN.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	OR	OFF	2007/07/30 15:45
S12	12	("4809075"   "5055667"   "5268567"   "5283426"   "5872596"   "6008486"   "6040570"   "6130713"   "6246043"   "6248991").PN. OR ("6348681").URPN.	US-PGPUB; USPAT; USOCR	ADJ	ON	2007/07/31 14:44

Day : Tuesday  
Date: 7/31/2007


**PALM INTRANET**

Time: 16:37:59

**Inventor Name Search Result**

Your Search was:

Last Name = LEE

First Name = HAE-SEUNG

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">07041773</a>	<a href="#">4860232</a>	150	04/22/1987	DIGITAL TECHNIQUE FOR PRECISE MEASUREMENT OF VARIABLE CAPACITANCE	LEE, HAE-SEUNG
<a href="#">07367387</a>	<a href="#">5028977</a>	150	06/16/1989	MERGED BIPOLAR AND INSULATED GATE TRANSISTORS	LEE, HAE-SEUNG
<a href="#">07628340</a>	<a href="#">5223754</a>	150	12/14/1990	RESISTIVE FUSE CIRCUITS FOR IMAGE SEGMENTATION AND SMOOTHING	LEE, HAE-SEUNG
<a href="#">07642665</a>	<a href="#">5193640</a>	150	01/17/1991	VEHICLE SAFETY SYSTEM FOR DRIVER PEDAL MISAPPLICATION	LEE, HAE-SEUNG
<a href="#">08060754</a>	Not Issued	161	05/12/1993	ALGORITHMIC A/D CONVERTER WITH DIGITALLY CALIBRATED OUTPUT	LEE, HAE-SEUNG
<a href="#">08122377</a>	<a href="#">5434538</a>	150	09/16/1993	GAIN ENHANCEMENT FOR AMPLIFIER USING A REPLICA AMPLIFIER	LEE, HAE-SEUNG
<a href="#">08170040</a>	<a href="#">5416485</a>	150	12/20/1993	ANALOG-TO-DIGITAL CONVERSION CIRCUIT WITH IMPROVED DIFFERENTIAL LINEARITY	LEE, HAE-SEUNG
<a href="#">08201250</a>	<a href="#">5499027</a>	150	02/24/1994	DIGITALLY SELF-CALIBRATING PIPELINE ANALOG-TO-DIGITAL CONVERTER	LEE, HAE-SEUNG
<a href="#">08338245</a>	Not Issued	161	11/14/1994	EVACUABLE GARMENT CONTAINER	LEE, HAE-SEUNG
<a href="#">08412269</a>	<a href="#">5510789</a>	150	03/28/1995	ALGORITHMIC A/D CONVERTER WITH DIGITALLY CALIBRATED OUTPUT	LEE, HAE-SEUNG
<a href="#">08909650</a>	<a href="#">6271784</a>	150	08/12/1997	CAPACITOR-BASED DIGITAL-TO-ANALOG CONVERTER	LEE, HAE-SEUNG

				WITH CONTINUOUS TIME OUTPUT	
<u>08931125</u>	<u>6832291</u>	150	09/16/1997	MEMORY SYSTEM FOR IMPROVING DATA INPUT/OUTPUT PERFORMANCE AND METHOD OF CACHING DATA RECOVERY INFORMATION	LEE, HAE-SEUNG
<u>08933756</u>	<u>5875965</u>	150	09/23/1997	AIR CIRCULATION SYSTEM FOR REDUNDANT ARRAYS OF INEXPENSIVE DISKS AND METHOD OF CONTROLLING AIR CIRCULATION	LEE, HAE-SEUNG
<u>08935201</u>	<u>6070249</u>	150	09/22/1997	SPLIT PARITY SPARE DISK ACHIEVING METHOD IN RAID SUBSYSTEM	LEE, HAE-SEUNG
<u>09153191</u>	<u>6157329</u>	150	09/15/1998	BANDPASS SIGMA-DELTA MODULATOR EMPLOYING HIGH-Q NARROWBAND NOISE SUPPRESSION	LEE, HAE-SEUNG
<u>09248154</u>	<u>6137431</u>	150	02/09/1999	OVERSAMPLED PIPELINE A/D CONVERTER WITH MISMATCH SHAPING	LEE, HAE-SEUNG
<u>09362577</u>	Not Issued	161	07/28/1999	BANDPASS MODULATOR	LEE, HAE-SEUNG
<u>09577881</u>	<u>6479928</u>	150	05/25/2000	CATHODE RAY TUBE	LEE, HAE-SEUNG
<u>09735219</u>	<u>6686860</u>	150	12/12/2000	RECONFIGURABLE ANALOG-TO-DIGITAL CONVERTER	LEE, HAE-SEUNG
<u>09916822</u>	<u>6600471</u>	150	07/27/2001	PRECISE MOS IMAGER TRANSFER FUNCTION CONTROL FOR EXPANDED DYNAMIC RANGE IMAGING	LEE, HAE-SEUNG
<u>10024913</u>	Not Issued	164	12/19/2001	DIGITAL CAMERA WITH POWER MANAGEMENT	LEE, HAE-SEUNG
<u>10053553</u>	<u>6891322</u>	150	01/24/2002	FILTER LAYER FOR A DISPLAY, A METHOD OF PREPARING A FILTER LAYER FOR A DISPLAY AND A DISPLAY INCLUDING A FILTER LAYER	LEE, HAE-SEUNG
<u>10067554</u>	<u>6777660</u>	150	02/04/2002	CMOS ACTIVE PIXEL WITH RESET NOISE REDUCTION	LEE, HAE-SEUNG
<u>10244723</u>	<u>6881992</u>	150	09/16/2002	CMOS PIXEL DESIGN FOR MINIMIZATION OF DEFECT-INDUCED LEAKAGE CURRENT	LEE, HAE-SEUNG

<u>10284938</u>	Not Issued	93	10/31/2002	PIXEL DESIGN INCLUDING IN-PIXEL CORRELATED DOUBLE SAMPLING CIRCUIT	LEE, HAE-SEUNG
<u>10335677</u>	7242429	150	01/02/2003	METHOD FOR CANCELLATION OF THE EFFECT OF CHARGE FEEDTHROUGH ON CMOS PIXEL OUTPUT	LEE, HAE-SEUNG
<u>10336482</u>	Not Issued	41	01/03/2003	Compact digital camera system	LEE, HAE-SEUNG
<u>10352467</u>	Not Issued	161	01/28/2003	ADC having chopper offset cancellation	LEE, HAE-SEUNG
<u>10400731</u>	Not Issued	93	03/27/2003	OPERATIONAL TRANSCONDUCTANCE AMPLIFIER FOR HIGH-SPEED, LOW-POWER IMAGING APPLICATIONS	LEE, HAE-SEUNG
<u>10679755</u>	6903670	150	10/06/2003	CIRCUIT AND METHOD FOR CANCELLATION OF COLUMN PATTERN NOISE IN CMOS IMAGERS	LEE, HAE-SEUNG
<u>10685121</u>	Not Issued	30	10/14/2003	CMOS pixel design for enabling a large fill factor	LEE, HAE-SEUNG
<u>10751562</u>	Not Issued	30	01/05/2004	Precise CMOS imager transfer function control for expanded dynamic range imaging using variable-height multiple reset pulses	LEE, HAE-SEUNG
<u>10752112</u>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	LEE, HAE-SEUNG
<u>10752131</u>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	LEE, HAE-SEUNG
<u>10752197</u>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	LEE, HAE-SEUNG
<u>10755655</u>	6864822	150	01/12/2004	RECONFIGURABLE ANALOG-TO-DIGITAL CONVERTER	LEE, HAE-SEUNG
<u>10841455</u>	Not Issued	93	05/10/2004	ORGANIC ELECTROLUMINESCENT DEVICE AND METHOD OF MANUFACTURING THE SAME	LEE, HAE-SEUNG
<u>10924880</u>	Not Issued	71	08/25/2004	Organic electroluminescent device with an absorbent for moisture and oxygen, and method for manufacturing the same	LEE, HAE-SEUNG
<u>10932111</u>	Not Issued	41	09/02/2004	Organic electroluminescent device and method of manufacturing the	LEE, HAE-SEUNG

				same	
<a href="#">10983953</a>	<a href="#">7002501</a>	150	11/08/2004	ANALOG-TO-DIGITAL CONVERTER HAVING PARAMETRIC CONFIGURABILITY	LEE, HAE-SEUNG
<a href="#">11046797</a>	Not Issued	71	02/01/2005	Organic electroluminescent display device	LEE, HAE-SEUNG
<a href="#">11085258</a>	Not Issued	41	03/22/2005	Organic electroluminescence device and manufacturing method thereof	LEE, HAE-SEUNG
<a href="#">11221480</a>	Not Issued	30	09/07/2005	Active pixel image sensor with selectable source follower and common source amplifier modes	LEE, HAE-SEUNG
<a href="#">11221538</a>	Not Issued	41	09/07/2005	Active pixel image sensor with common gate amplifier mode	LEE, HAE-SEUNG
<a href="#">11221612</a>	Not Issued	30	09/07/2005	Active pixel image sensor with low noise reset	LEE, HAE-SEUNG
<a href="#">11248947</a>	Not Issued	30	10/11/2005	Low noise active pixel image sensor	LEE, HAE-SEUNG
<a href="#">11250114</a>	Not Issued	30	10/12/2005	Low noise active pixel image sensor	LEE, HAE-SEUNG
<a href="#">11267185</a>	Not Issued	30	11/07/2005	Organic electroluminescent device and method for manufacturing the same	LEE, HAE-SEUNG
<a href="#">11343064</a>	Not Issued	95	01/30/2006	COMPARATOR-BASED SWITCHED CAPACITOR CIRCUIT FOR SCALED SEMICONDUCTOR FABRICATION PROCESSES	LEE, HAE-SEUNG
<a href="#">11454275</a>	Not Issued	30	06/16/2006	Sampled-data circuits using zero crossing detection	LEE, HAE-SEUNG


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<b>Search Another: Inventor</b>	<b>Last Name</b>	<b>First Name</b>	
	LEE	HAE-SEUNG	<input type="button" value="Search"/>

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Time: 16:38:12

**Inventor Name Search Result**

Your Search was:

Last Name = FIFE

First Name = KEITH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<a href="#">60329962</a>	Not Issued	159	10/17/2001	Compact digital camera system	FIFE, KEITH
<a href="#">09916822</a>	<a href="#">6600471</a>	150	07/27/2001	PRECISE MOS IMAGER TRANSFER FUNCTION CONTROL FOR EXPANDED DYNAMIC RANGE IMAGING	FIFE, KEITH G.
<a href="#">11221480</a>	Not Issued	30	09/07/2005	Active pixel image sensor with selectable source follower and common source amplifier modes	FIFE, KEITH G.
<a href="#">11221538</a>	Not Issued	41	09/07/2005	Active pixel image sensor with common gate amplifier mode	FIFE, KEITH G.
<a href="#">11221612</a>	Not Issued	30	09/07/2005	Active pixel image sensor with low noise reset	FIFE, KEITH G.
<a href="#">11248947</a>	Not Issued	30	10/11/2005	Low noise active pixel image sensor	FIFE, KEITH G.
<a href="#">11250114</a>	Not Issued	30	10/12/2005	Low noise active pixel image sensor	FIFE, KEITH G.
<a href="#">60221856</a>	Not Issued	159	07/28/2000	CMOS imager with on-chip barrier voltage generation	FIFE, KEITH G.
<a href="#">60256782</a>	Not Issued	159	12/19/2000	Retractable digital camera	FIFE, KEITH G.
<a href="#">60261023</a>	Not Issued	159	01/11/2001	Adaptive sensitivity control for a digital imager	FIFE, KEITH G.
<a href="#">60322361</a>	Not Issued	159	09/14/2001	CMOS pixel design for minimization of defect-induced leakage current	FIFE, KEITH G.
<a href="#">60345780</a>	Not Issued	159	01/03/2002	Method for cancellation of the effect of charge feedthrough on CMOS pixel output	FIFE, KEITH G.
<a href="#">60368904</a>	Not Issued	159	03/29/2002	Operational transconductance amplifier for high-speed, low-power imaging applications	FIFE, KEITH G.

<u>60416043</u>	Not Issued	159	10/04/2002	Method for cancellation of column pattern noise in CMOS imagers	FIFE, KEITH G.
<u>60417964</u>	Not Issued	159	10/11/2002	CMOS pixel design for enabling a large fill factor	FIFE, KEITH G.
<u>60417978</u>	Not Issued	159	10/11/2002	In-stream lossless compression of digital image sensor data	FIFE, KEITH G.
<u>60438683</u>	Not Issued	159	01/08/2003	Precise CMOS imager transfer function control for expanded dynamic range imaging using variable-height multiple reset pulses	FIFE, KEITH G.
<u>60438699</u>	Not Issued	159	01/08/2003	CMOS active pixel with hard and soft reset	FIFE, KEITH G.
<u>60607541</u>	Not Issued	159	09/07/2004	Low noise flexible architecture active pixel image sensor	FIFE, KEITH G.
<u>60617900</u>	Not Issued	159	10/12/2004	Low noise active pixel image sensor	FIFE, KEITH G.
<u>10024913</u>	Not Issued	164	12/19/2001	DIGITAL CAMERA WITH POWER MANAGEMENT	FIFE, KEITH GLEN
<u>10037885</u>	Not Issued	161	01/04/2002	Adaptive sensitivity control, on a pixel-by-pixel basis, for a digital imager	FIFE, KEITH GLEN
<u>10225925</u>	<u>7173666</u>	150	08/22/2002	SYSTEM AND METHOD FOR DISPLAYING A NON-STANDARD ASPECT RATIO IMAGE ON A STANDARD ASPECT RATIO MONITOR	FIFE, KEITH GLEN
<u>10244723</u>	<u>6881992</u>	150	09/16/2002	CMOS PIXEL DESIGN FOR MINIMIZATION OF DEFECT-INDUCED LEAKAGE CURRENT	FIFE, KEITH GLEN
<u>10335677</u>	<u>7242429</u>	150	01/02/2003	METHOD FOR CANCELLATION OF THE EFFECT OF CHARGE FEEDTHROUGH ON CMOS PIXEL OUTPUT	FIFE, KEITH GLEN
<u>10336482</u>	Not Issued	41	01/03/2003	Compact digital camera system	FIFE, KEITH GLEN
<u>10400731</u>	Not Issued	93	03/27/2003	OPERATIONAL TRANSCONDUCTANCE AMPLIFIER FOR HIGH-SPEED, LOW-POWER IMAGING APPLICATIONS	FIFE, KEITH GLEN
<u>10679755</u>	<u>6903670</u>	150	10/06/2003	CIRCUIT AND METHOD FOR CANCELLATION OF COLUMN PATTERN NOISE IN CMOS	FIFE, KEITH GLEN



				IMAGERS	
<a href="#">10685121</a>	Not Issued	30	10/14/2003	CMOS pixel design for enabling a large fill factor	FIFE, KEITH GLEN
<a href="#">10685222</a>	Not Issued	41	10/14/2003	In-stream lossless compression of digital image sensor data	FIFE, KEITH GLEN
<a href="#">10751562</a>	Not Issued	30	01/05/2004	Precise CMOS imager transfer function control for expanded dynamic range imaging using variable-height multiple reset pulses	FIFE, KEITH GLEN
<a href="#">10752112</a>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	FIFE, KEITH GLEN
<a href="#">10752131</a>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	FIFE, KEITH GLEN
<a href="#">10752197</a>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	FIFE, KEITH GLEN
<a href="#">11642579</a>	Not Issued	30	12/19/2006	System and method for displaying a non-standard aspect ratio image on a standard aspect ratio monitor	FIFE, KEITH GLEN
<a href="#">07300647</a>	Not Issued	89	01/05/1989	PROCESSING OF PLUTONIUM	FIFE, KEITH W.
<a href="#">07380475</a>	Not Issued	89	07/06/1989	REMOVAL OF IMPURITIES FROM PLUTONIUM	FIFE, KEITH W.

Inventor Search Completed: No Records to Display.


Search Another: Inventor
 

Last Name	First Name
<input type="text" value="FIFE"/>	<input type="text" value="KEITH"/>

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Day : Tuesday  
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Time: 16:38:26

**Inventor Name Search Result**

Your Search was:

Last Name = BROOKS

First Name = LANE

Application#	Patent#	Status	Date Filed	Title	Inventor Name
<u>60329962</u>	Not Issued	159	10/17/2001	Compact digital camera system	BROOKS, LANE
<u>60845863</u>	Not Issued	20	09/19/2006	Sequence independent non-overlapping digital signal generator with programmable delay	BROOKS, LANE
<u>10685222</u>	Not Issued	41	10/14/2003	In-stream lossless compression of digital image sensor data	BROOKS, LANE C.
<u>09837827</u>	Not Issued	161	04/18/2001	Efficient digital method of and system for determining the instantaneous phase and amplitude of a vibratory accelerometer and other sensors	BROOKS, LANE G.
<u>10691866</u>	Not Issued	41	10/23/2003	Efficient digital method of and system for determining the instantaneous phase and amplitude of a vibratory accelerometer and other sensors	BROOKS, LANE G.
<u>10751562</u>	Not Issued	30	01/05/2004	Precise CMOS imager transfer function control for expanded dynamic range imaging using variable-height multiple reset pulses	BROOKS, LANE G.
<u>10752112</u>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	BROOKS, LANE G.
<u>10752131</u>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	BROOKS, LANE G.
<u>10752197</u>	Not Issued	41	01/06/2004	CMOS active pixel with hard and soft reset	BROOKS, LANE G.
<u>60417978</u>	Not Issued	159	10/11/2002	In-stream lossless compression of digital image sensor data	BROOKS, LANE G.
<u>10024913</u>	Not Issued	164	12/19/2001	DIGITAL CAMERA WITH POWER MANAGEMENT	BROOKS, LANE GEARLE

<a href="#">10037885</a>	Not Issued	161	01/04/2002	Adaptive sensitivity control, on a pixel-by-pixel basis, for a digital imager	BROOKS, LANE GEARLE
<a href="#">10336482</a>	Not Issued	41	01/03/2003	Compact digital camera system	BROOKS, LANE GEARLE
<a href="#">60438683</a>	Not Issued	159	01/08/2003	Precise CMOS imager transfer function control for expanded dynamic range imaging using variable-height multiple reset pulses	BROOKS, LANE GEARLE
<a href="#">60438699</a>	Not Issued	159	01/08/2003	CMOS active pixel with hard and soft reset	BROOKS, LANE GEARLE

Inventor Search Completed: No Records to Display.

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